

Fig. 8

ANNOTATED SHEET

Title: Controller Data Sharing Using a Modular DMA Architecture

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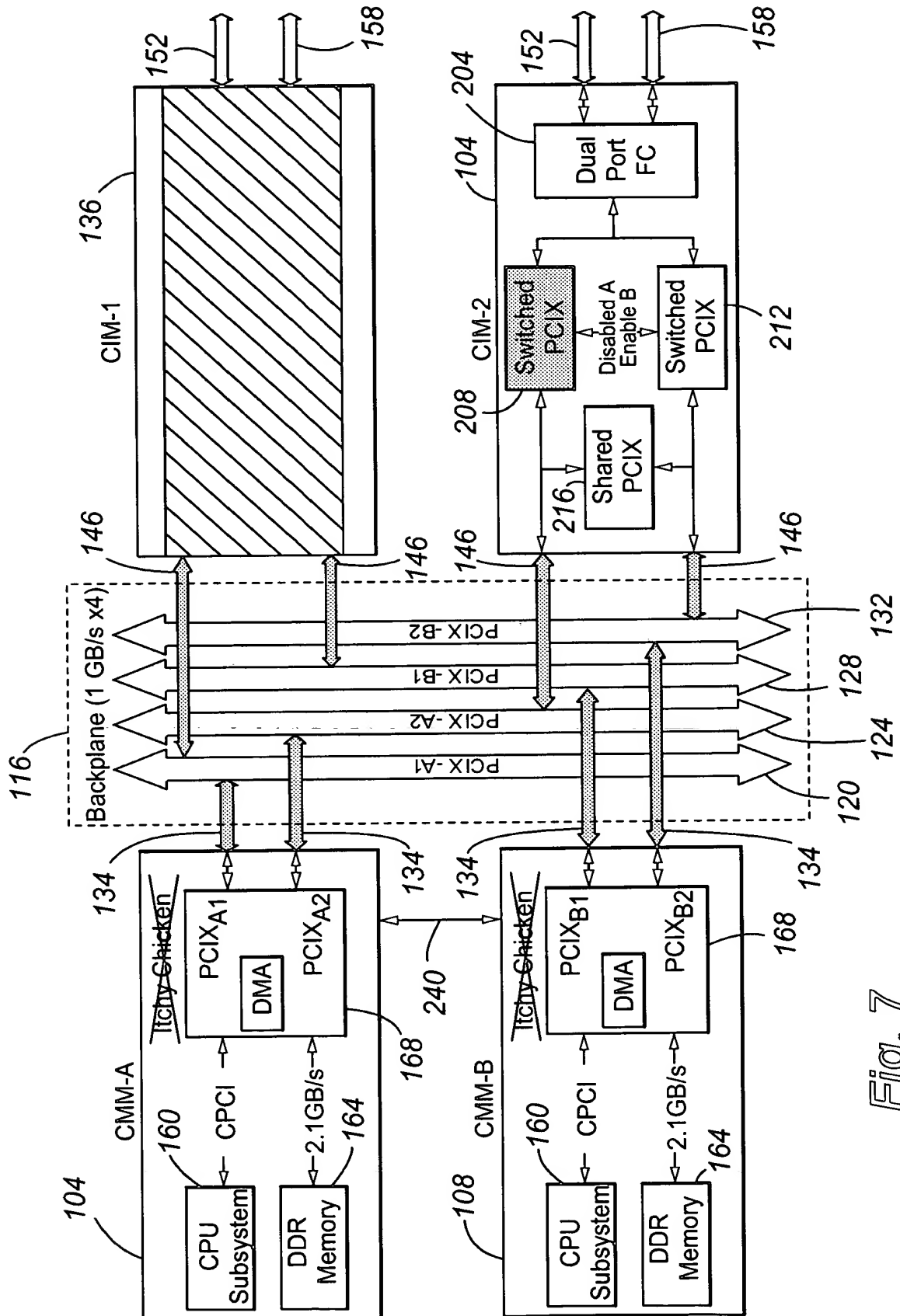


Fig. 7

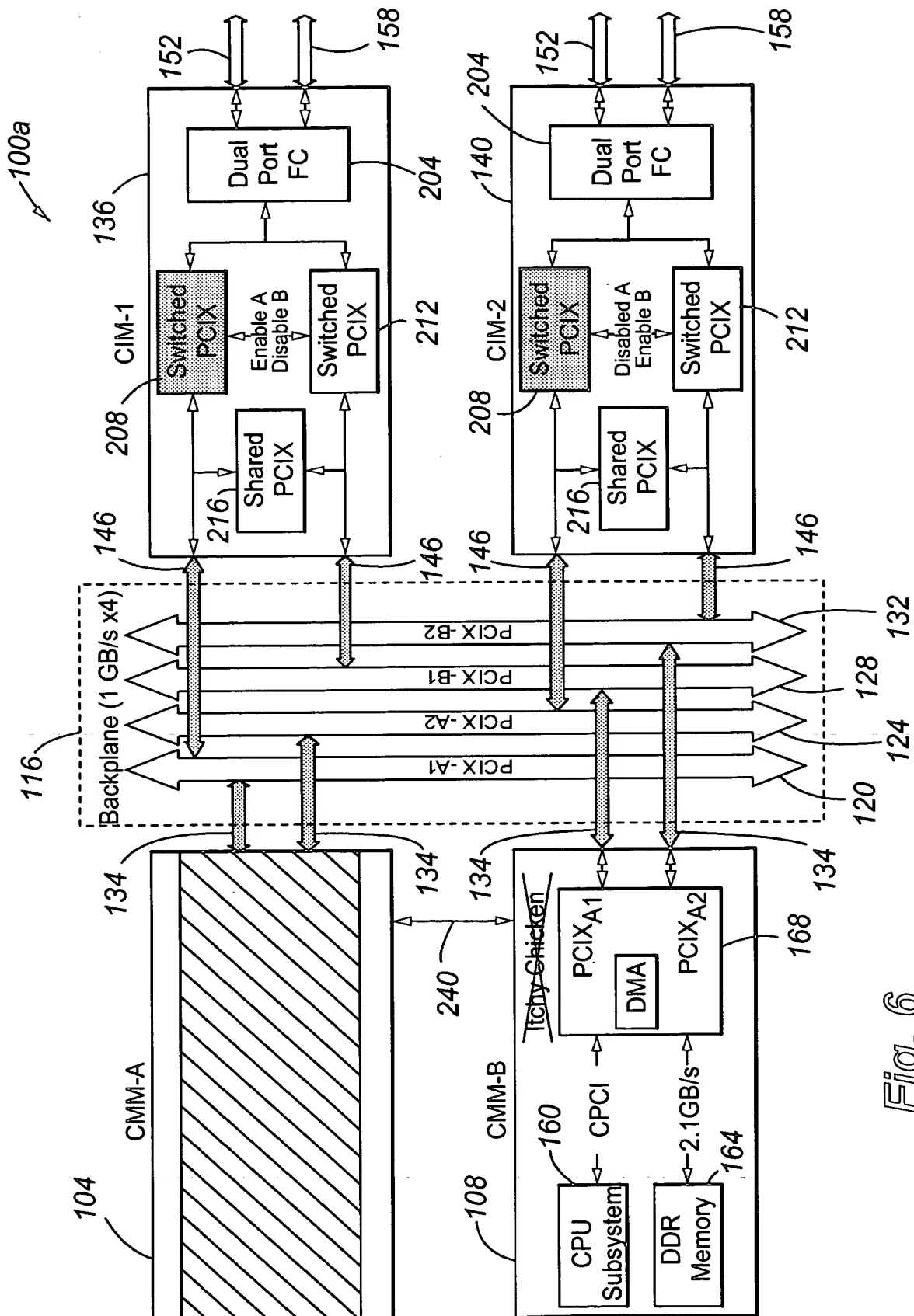


Fig. 6

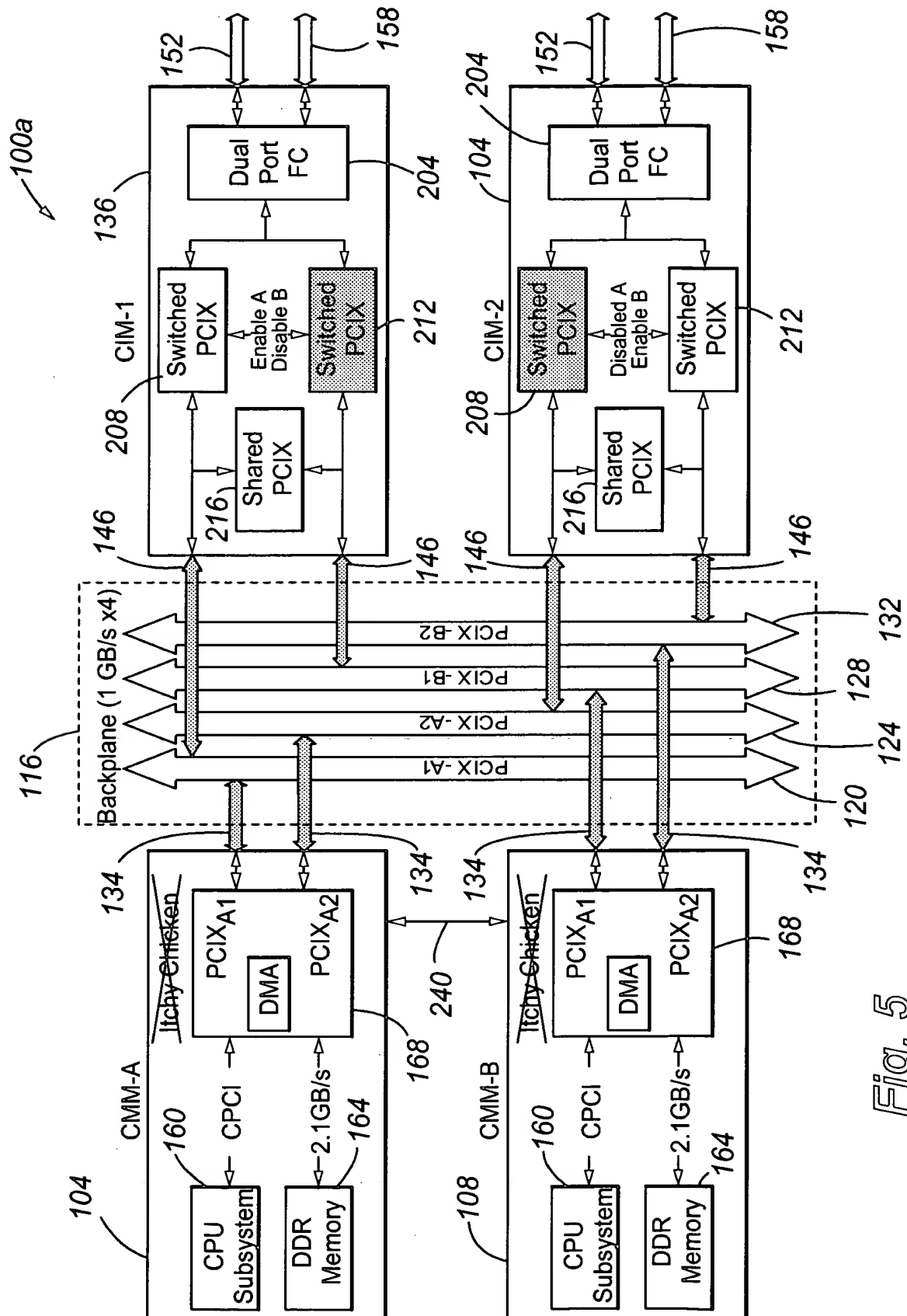


Fig. 5